

The schematic illustrates a complex video processing system, likely a VCR or camcorder control unit. It features numerous integrated circuits (ICs) and their interconnections:

- Phase-Locked Loops (VCOs):** IC10<1> (VCO 28.6MHz(NTSC)/28.8MHz(PAL)), IC17<2> (13.5MHz VCO), IC24<2> (14.3MHz(NTSC)/17.7MHz(PAL) VCO), and IC19<2> (14.3MHz(NTSC)/17.7MHz(PAL) VCO).
- Sync Generators:** IC27<2> (SYNC GEN), IC11<1>, and IC18<1>.
- Decoders:** IC11<1> (H/V SYNC SEP), IC11<1> (DECODER), and IC11<1> (H COUNTER 7bit).
- Counters:** V COUNTER 4bit, V COUNTER 10bit, H COUNTER 10bit, and H COUNTER 7bit.
- Multiplexers and Selectors:** DATA SELECTOR, MULTI-PLEXER, and SELECT.
- Test Points:** TP1 through TP16 are distributed throughout the circuit for testing.

A central section contains a table for SCC1 and SCC2 settings for different angles (0°, 90°, 180°, 270°):

	0°	90°	180°	270°
SCC1	L	H	L	H
SCC2	L	L	H	H

The diagram also shows connections to various external components and pins, including CAMERA SYSCON(P1-50A) through P1-23B, MOTHER(P9601-23A,B)-PRE PROCESS(P3-2) through P3-17, ENCODER(P1-36B) through P1-37B, and VIDEO I/F(P1-18A).